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By Stephen C. Thierauf

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Editorial Review

About the Author

Stephen C. Thierauf served as senior consulting hardware engineer at Fabric Networks, Inc. While working at Compaq Computers and Digital Equipment Corporation, he was a senior member of the technical staff responsible for the design and signal integrity analysis of high-speed I/O circuitry and interconnect on the ALPHA microprocessor. Formerly a visiting scholar at Northeastern University, he holds a B.S. in engineering technology from Wentworth College of Technology, Boston, MA.

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